

SHEET INDEX

CONTENTS	SHEET NO.	ISSUE NO.																									
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	
SHEET INDEX SUPPORTING INFORMATION	A1	1	2	3	4																						
FS 1 CONTROL PANEL CKT	B1	1	2	2	2																						
APP FIG. 1	C1	1	2	2	2																						
NOTES: CIRCUIT EQUIPMENT INFORMATION	D1	1	2	2	2																						
	D2		2	3	3																						
CPS 1 CONTROL PANEL CKT	J1	1	2	3	4																						
	J2	1	2	2	2																						
	J3	1	2	3	3																						
	J4	1	2	2	2																						

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

SUPPORTING INFORMATION

CATEGORY	NO.
EQUIPMENT DRAWING	ED-4C006-30

NOTICE- NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

ISSUE
4D

1N98

COMMON SYSTEMS
SACC CONTROL PANEL
CIRCUITAT&T
STANDARD

②

SD-1C901-01-A1
9 SHEETSBELL TELEPHONE LABORATORIES
INCORPORATED

6S

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FS I CONTROL PANEL CKT

TO PROCESSOR
FRAME CKT
(SEE TABLE B
AND NOTE 302)

TO 3ACC CKT
(SEE TABLE A)

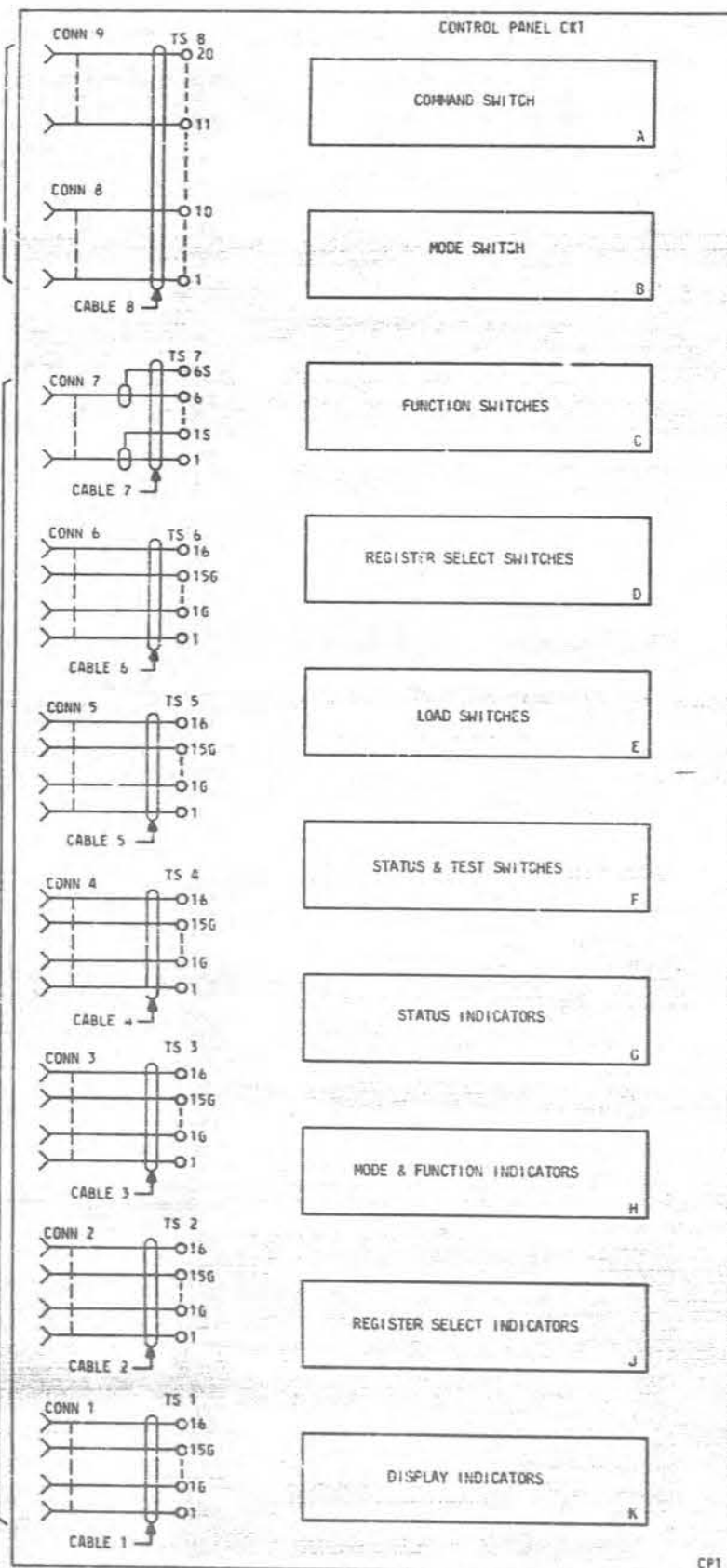


TABLE A

PANEL CONNECTION			3ACC CKT CONNECTION	
TS NO.	TERM. NO.	LEAD DESIG	EQPT LOC	TERM. NO.
1	1		10-20	218
	2			318
	3			217
	4	MANL1		317
	5	STBPL1		216
	6	BNL1		316
	7	WFFL1		215
	8			315
	9			214
	10	LBADKO		314
	11	DISRKO		213
	12	ENABLO		313
	13	HALTLO		212
	14	ROLO		312
	15	SPLLO		211
	16			311
16-15G				310
2	1	R1LO	10-21	018
	2	R2LO		118
	3	R3LO		017
	4	R4LO		117
	5	R5LO		016
	6	R6LO		116
	7	R7LO		015
	8	R8LO		115
	9	R9LO		014
	10	R10LO		114
	11	R11LO		013
	12	R12LO		113
	13	R13LO		012
	14	R14LO		112
	15	R15LO		011
	16	FLKO		111
16-15G				110
3	1	PHKO	10-21	218
	2	MPKO		318
	3	PHLO		217
	4	PLLO		317
	5	DB19LO		216
	6	DB18LO		316
	7	DB13LO		215
	8	DB12LO		315
	9	DB11LO		214
	10	DB10LO		314
	11	DB9LO		213
	12	DB14LO		313
	13	DB15LO		212
	14	DB16LO		312
	15	DB17LO		211
	16	MPLO		311
16-15G				310
4	1		10-20	208
	2			308
	3			207
	4	REJLO		307
	5	BAEXKO		206
	6	STEPKO		306
	7	HALTKO		205
	8	RS1KO		305
	9	HILMKO		204
	10	RS2KO		304
	11	RS4KO		203
	12	RS6KO		303
	13	DISMKO		202
	14	STBKO		302
	15	IADRKO		201
	16	CDATKO		301
16-15G				300

TABLE B

PANEL CONNECTION			PROCESSOR FRAME CKT CONNECTION (SEE NOTE 302)	
TS NO.	TERM. NO.	LEAD DESIG	EQPT LOC	TERM. NO.
5	1	CAORKO	10-21	008
	2	SGKO		108
	3	I15KO		007
	4	I14KO		107
	5	I13KO		006
	6	I12KO		106
	7	I11KO		005
	8	I10KO		105
	9	I9KO		004
	10	I8KO		104
	11	I7KO		003
	12	I6KO		103
	13	I5KO		002
	14	I4KO		102
	15	I16KO		001
	16	I17KO		101
16-15G				100
6	1	I0KO	10-21	208
	2	I18KO		308
	3	I19KO		207
	4	I1KO		307
	5	I2KO		206
	6	I3KO		306
	7			205
	8	DB4LO		305
	9	DB3LO		204
	10	DB2LO		304
	11	DB1LO		203
	12	DB0LO		303
	13	DB5LO		202
	14	DB6LO		302
	15	DB7LO		201
	16	DB8LO		301
16-15G				300
7	1	EXECKO	10-20	118
	2	EXECKOG		018
	3	EXECK1		117
	4	EXECK1G		017
	5	RESKO		116
	6	RESKOG		016
	7	RESK1		115
	8	RESK1G		015
	9	MANKO		114
	10	MANKOG		014
	11	TMKO		113
	12	TMKOG		013
	13			
	14			
	15			
	16			
16-15G				

3ACC CONTROL PANEL

2

SD-1C901-01-B1

BELL TELEPHONE LABORATORIES

6S

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2D

APP FIG. 1

PANEL BACKPLANE ASSEMBLY		
DESIG	LOC	CODE
CONTROL PANEL CKT	1B2	ED-4C006

2D

SACC CONTROL PANEL

2

SD-1C901-01-C1

BELL TELEPHONE LABORATORIES
INCORPORATED

65

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DES IGS	FUSE AMP	POTENTIAL	ONE PER

<u>BATTERY SYMBOL</u>	<u>VOLTAGE RANGE</u>

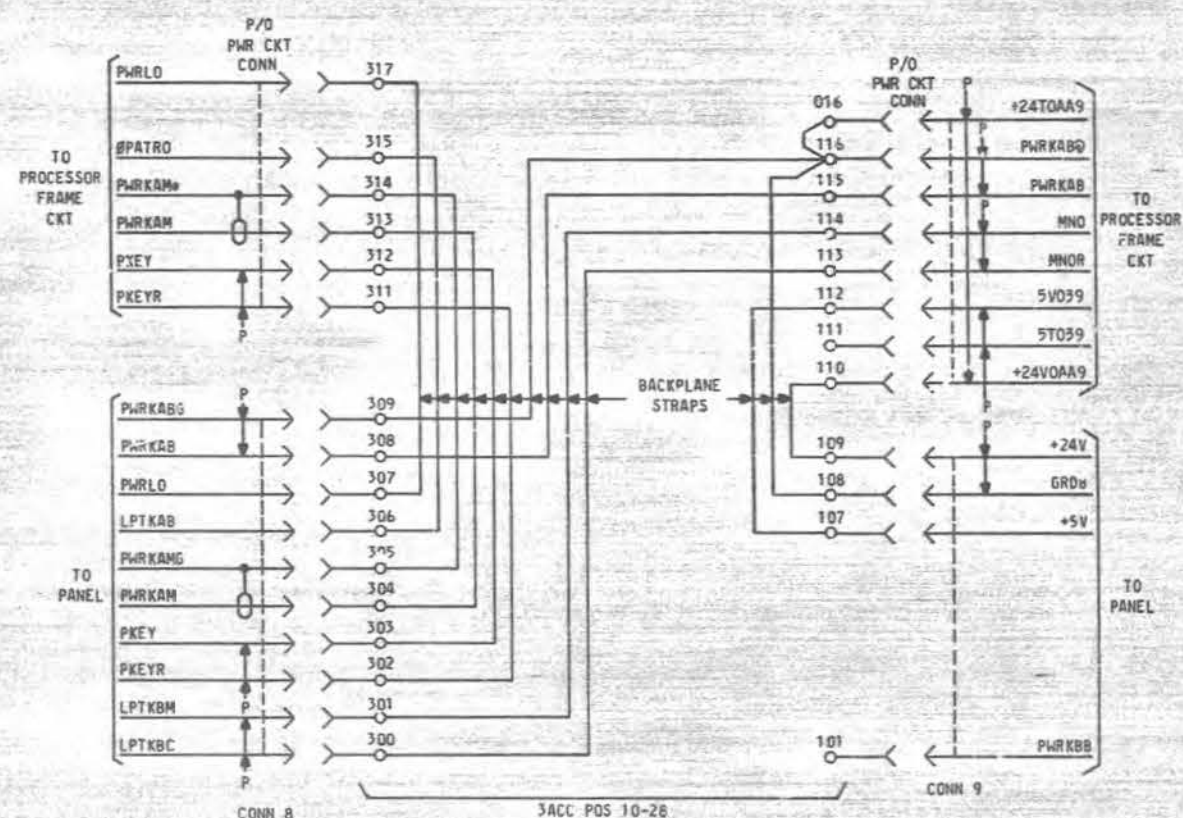
102.	FEATURE OR OPTION	PROVIDE	
		APP FIG.	APP OR WRG
			QUANTITY

[illegible]

201. ALL SWITCHES & INDICATORS CAN BE OBTAINED FROM CUTLER HAMMER.

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS.
CAPACITANCE VALUES ARE IN MICROFARADS
VALUES PRECEDED BY THE SYMBOL +(PLUS) OR
-(MINUS) ARE IN VOLTS.

302. CONNECTION TO PROCESSOR FRAME CKT IS VIA CC
BACKPLANE AS FOLLOWS:



LOAD AND DISPLAY																						
☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐			
EVABLE MANUAL PAR	PH	PL	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐

REGISTER SELECT																					
ADRS IN	ADRS MASK	DATA IN	DATA MASK	ERR	HOLD GET	INT MASK	INT SET	MC+ BUFR	M MEM STAT	MTCE STA	PROG ADRS	ST ADRS	SYS STAT	TIM	MCTL STAT	SPECIAL	☐	☐	☐	☐	
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	GENERAL	☐	8	4	2	1
AA	DFH	CA	PFM				EA	GR	KM	LF/RF	LM	LR	LW			EXTENDED					

REGISTER		COMPARE		MEMORY				MODE		COMMAND				
LOAD	DISPLAY	ADR	ENABLED	DATA	INCR ADR	STORE	DISPLAY	HIGH BITS	HALT	HALTED	STEP	EXTENDED	REJECT	EXECUTE
☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐
								LOW BITS				BASIC		

STATUS							
POWER	ACTIVE	NOT ACTIVE	MANUAL	ERROR STOPPED	RESET CIRCUITS	TEST MODE	LAMP & POWER TEST

PART OF CPS I CONTROL PANEL CKT

COMPONENT LIST

CABLE

DESIG	CODE
CABLE 1	ED-4C048-10 G1
CABLE 2	ED-4C048-10 G2
CABLE 3	ED-4C048-10 G3
CABLE 4	ED-4C048-10 G4
CABLE 5	ED-4C048-10 G5
CABLE 6	ED-4C048-10 G6
CABLE 7	ED-4C048-10 G7
CABLE 8	ED-4C048-10 G8

CAPACITOR

DESIG	CODE
C1	602C, 20
C2	601A, 5

DIODE

DESIG	CODE
[4] CR1.0-CR1.3	458C
[2] CR2.0, CR2.1	458C
[44] CR3.00-CR3.43	550B 534E

INDICATOR (SEE NOTE 201)

DESIG	CODE
ERROR STOPPED	SBTDV441-5 KS-21608, L102
MANUAL	SBTDV441-2 KS-21608, L2 (PART OF MANUAL SW ASSY)
NOT ACTIVE	SBTDV441-3 KS-21608, L101
ACTIVE	SBTDV441-6 KS-21608, L103
TEST MODE	SBTDV441-5 KS-21608, L102
PMR ON	SBTDV441-6 KS-21608, L1 (PART OF PMR SW ASSY)

RESISTOR

DESIG	CODE
[44] R1.00-R1.43	KS-20200 L1, 270
[4] R2.0-R2.3	KS-16645 L1, 6.2KΩ
[4] R3.0-R3.3	KS-20200 L1, 1.3KΩ
[4] R4.0-R4.3	KS-20200 L1, 1KΩ
[2] R5.0, R5.1	KS-16645 L1, 6.2KΩ

SWITCHES (SEE NOTE 201)

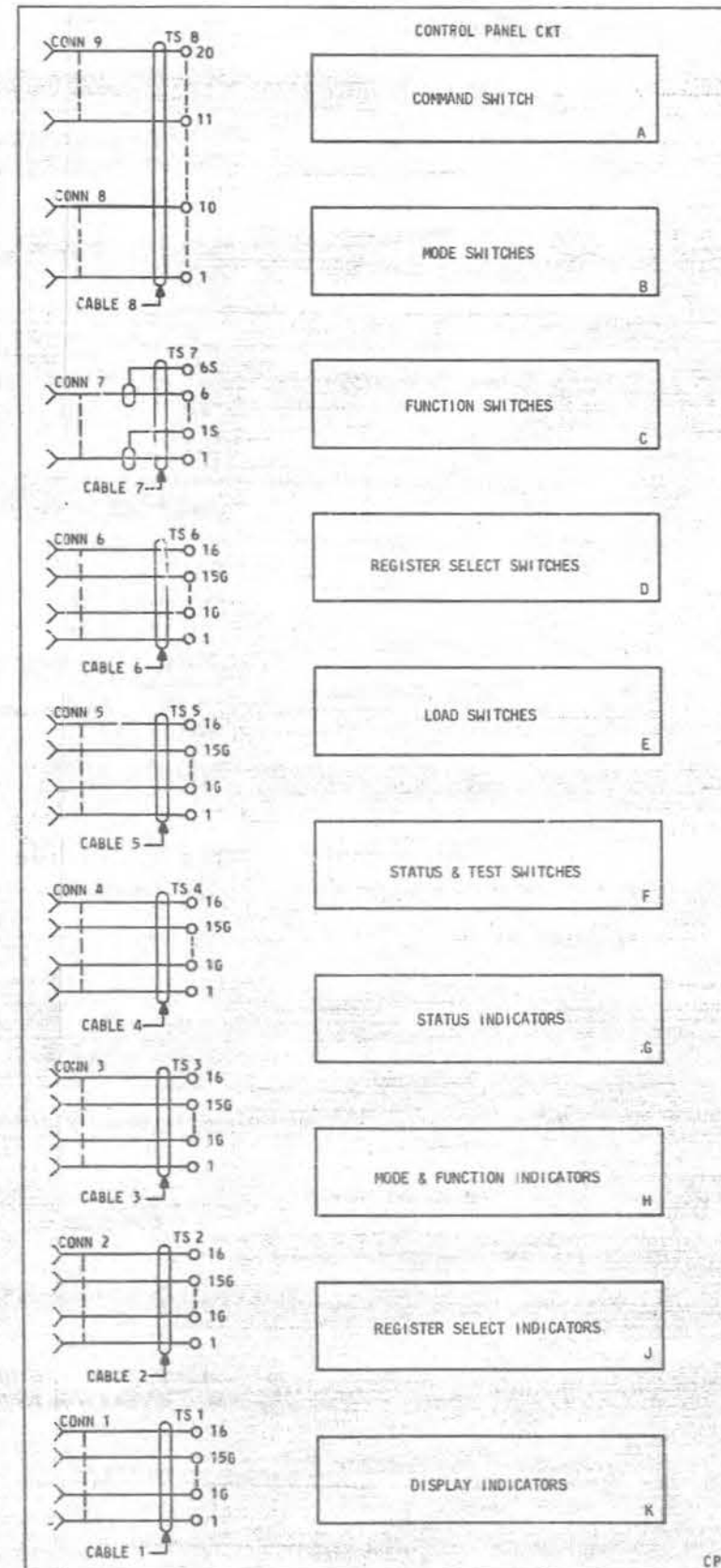
DESIG	CODE
BAPX	8911-K241 KS-21609, L1
CMP ADR	
CMP DATA	
DISM	
DISR	8911-K241 KS-21609, L1
EN MAN PAR	8911-K243 KS-21609, L3
EXEC	8911-K244 KS-21609, L4
HALT	8911-K241 KS-21609, L1
HIL0	
INC ADR	
0	
1	
2	8911-K241 KS-21609, L1
3	8911-K242 KS-21609, L2
4	8911-K242 KS-21609, L2
5	8911-K242 KS-21609, L2
6	8911-K241 KS-21609, L1
7	8911-K243 KS-21609, L1
8	8911-K241 KS-21609, L1
9	8911-K242 KS-21609, L2

SWITCHES (CONT)

DESIG	CODE
10	8911-K242 KS-21609, L2
11	8911-K242 KS-21609, L2
12	8911-K241 KS-21609, L1
13	8911-K241 KS-21609, L1
14	8911-K241 KS-21609, L1
15	8911-K242 KS-21609, L2
16	8911-K242 KS-21609, L2
17	8911-K242 KS-21609, L2
18	8911-K241 KS-21609, L1
19	8911-K241 KS-21609, L1
LAMP & PMR TEST	SBTDV441-2 KS-21608, L5
LOAD	8911-K241 KS-21609, L1
MANUAL	SBTDV441-2 KS-21608, L2
PH	8911-K243 KS-21609, L3
PL	8911-K243 KS-21609, L3
PMR	SBTDV441-6 KS-21608, L1
REGSEL	8911-K241 KS-21609, L1
REGSEL1	
REGSEL2	
REGSEL4	
REGSEL8	8911-K241 KS-21609, L1
RES CKT	SBTDV441-2 KS-21608, L4
STEP	8911-K241 KS-21609, L1
ST0	8911-K241 KS-21609, L1
TEST MODE	KS21027-E231

EQUIPPED
WITH
LAMP
388

SYMBOL



MANUFACTURING REFERENCES

CATEGORY	NO.

3ACC CONTROL PANEL

BELL TELEPHONE LABORATORIES
INCORPORATED

SD-1C901-01-J1

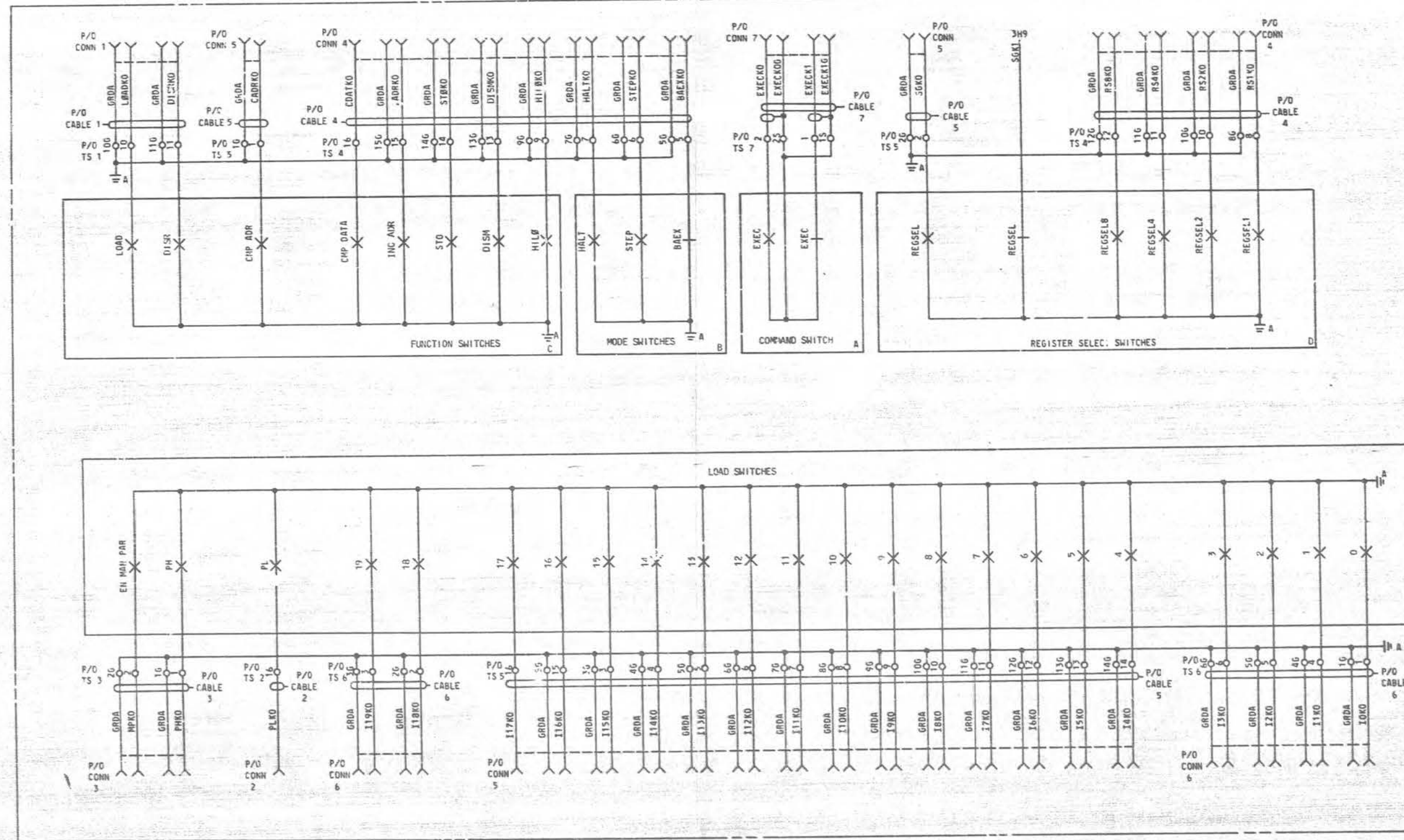
2
6S

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PART OF CPS I

CONTROL PANEL CRTS



SACC CONTROL PANEL

BELL TELEPHONE LABORATORIES
INCORPORATED

2

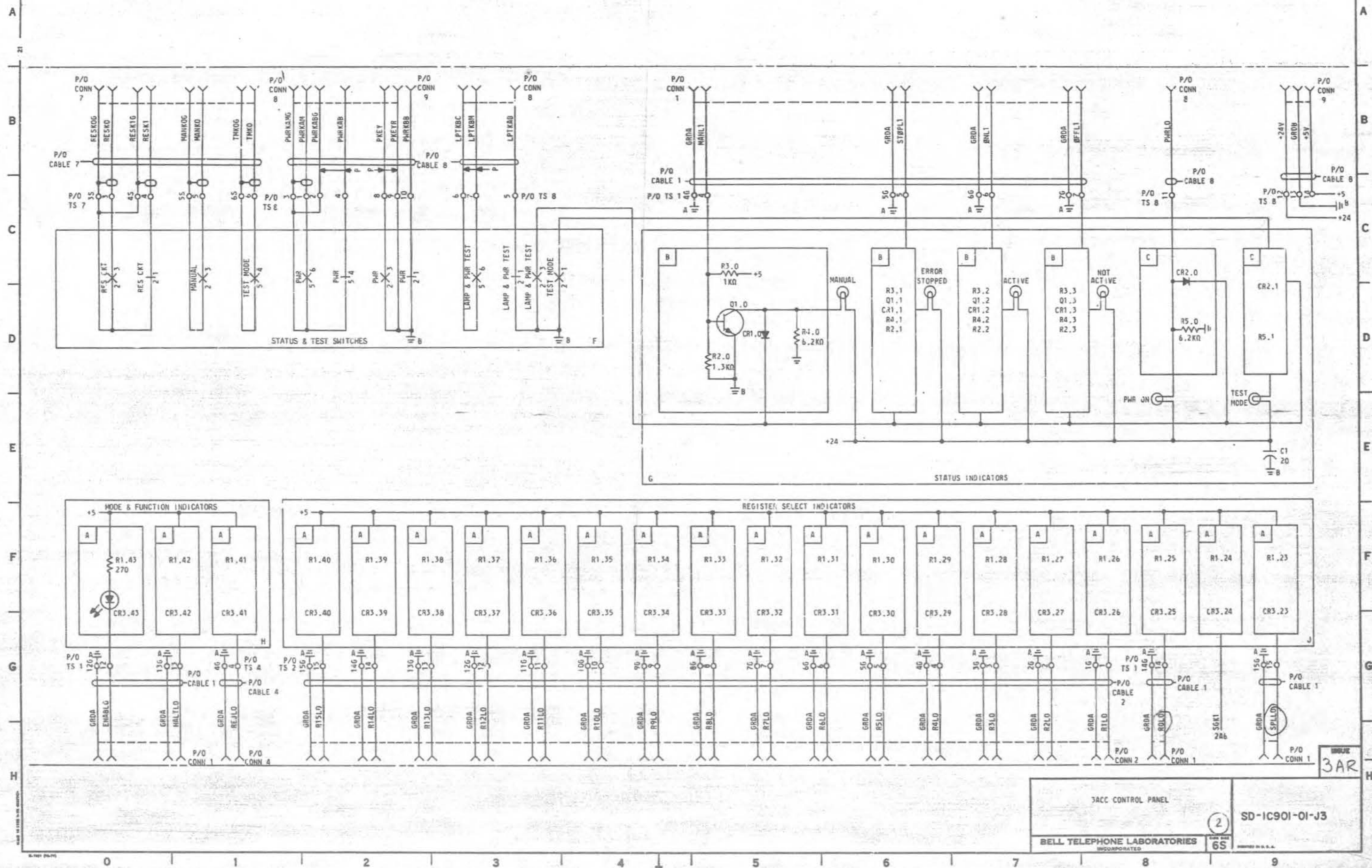
SD-1C901-01-J2

6S

2D

PART OF CPS I

CONTROL PANEL CKTS



PART OF CPS I

CONTROL PANEL CKTS

